

ALCA 1910

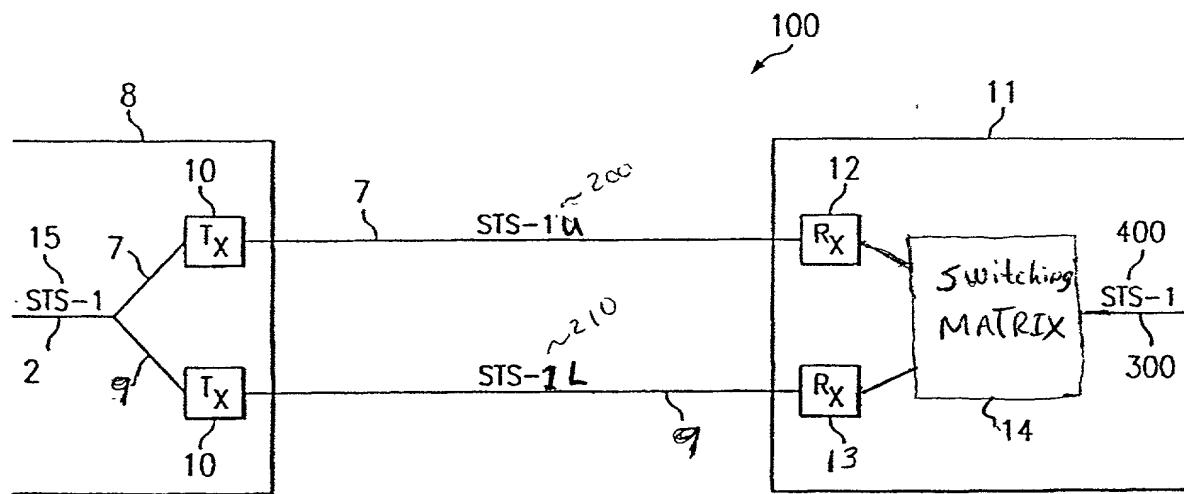
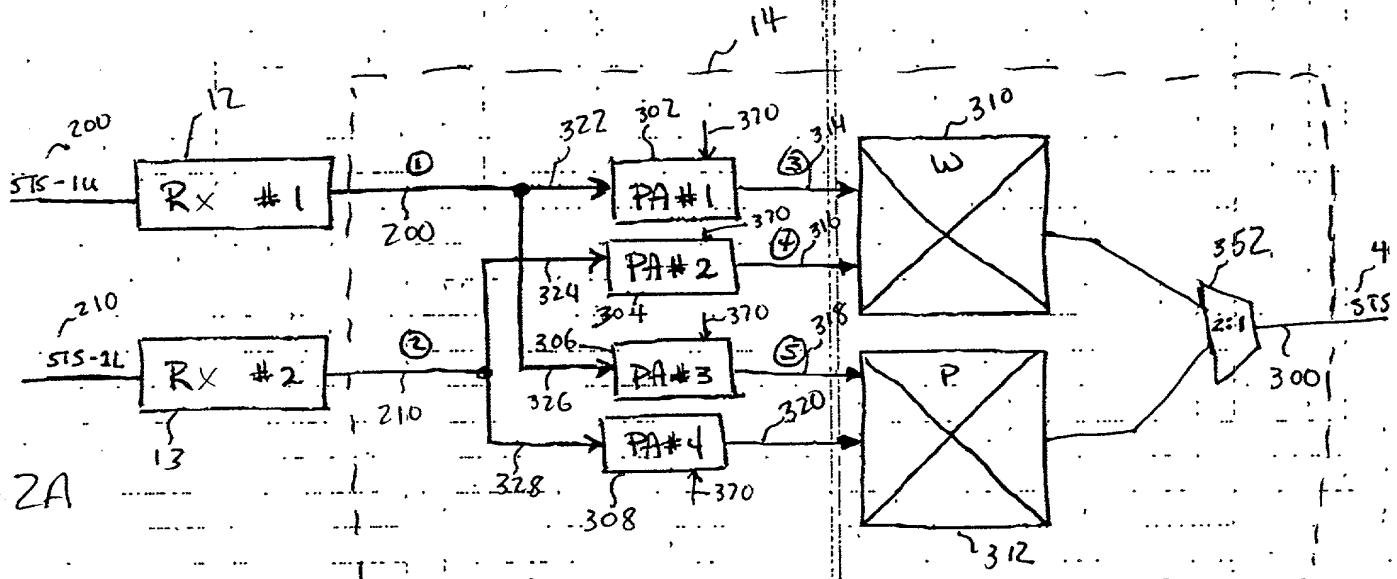


FIG. 1

Alcatel 1910



6 2A

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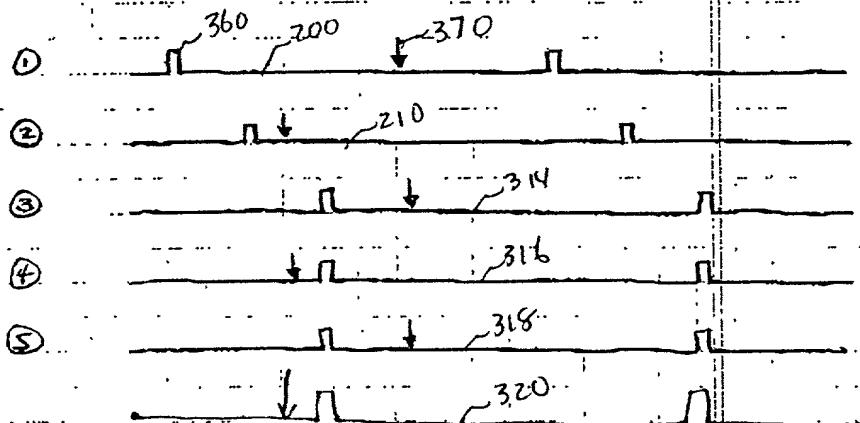


FIG. 2B

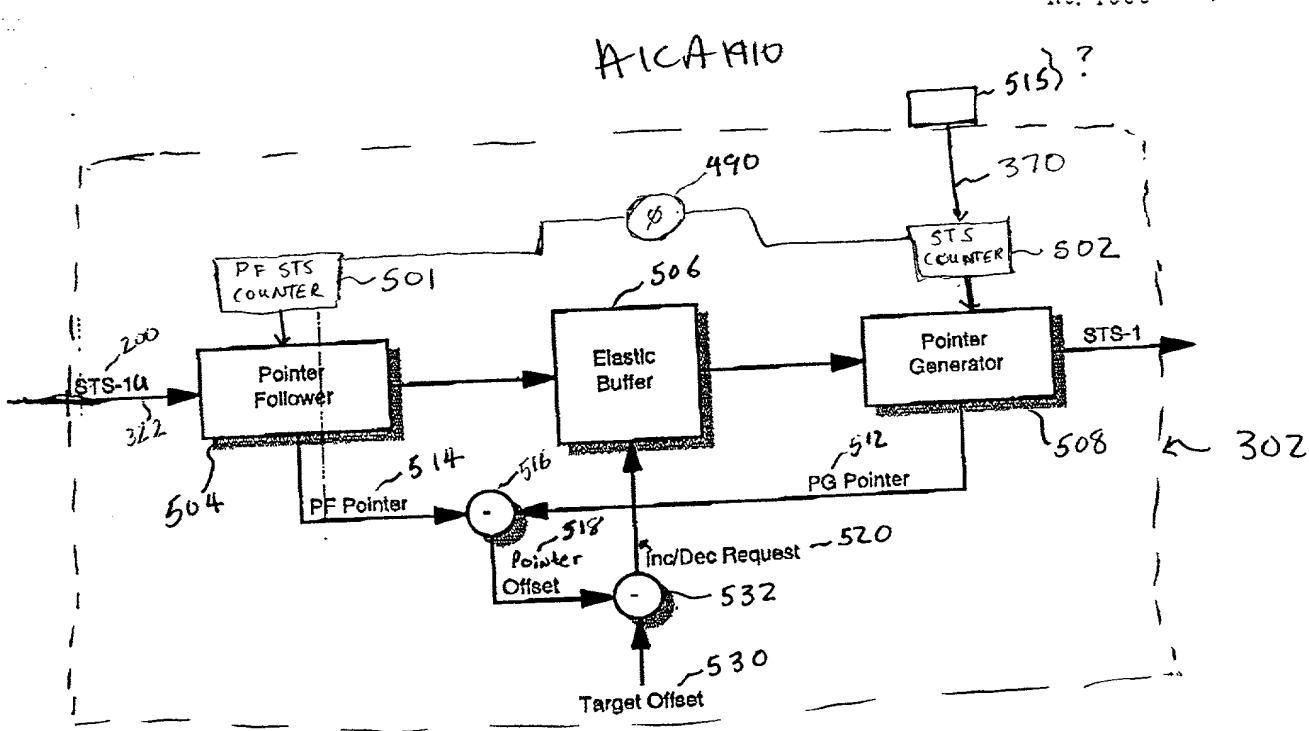


FIG. 3.

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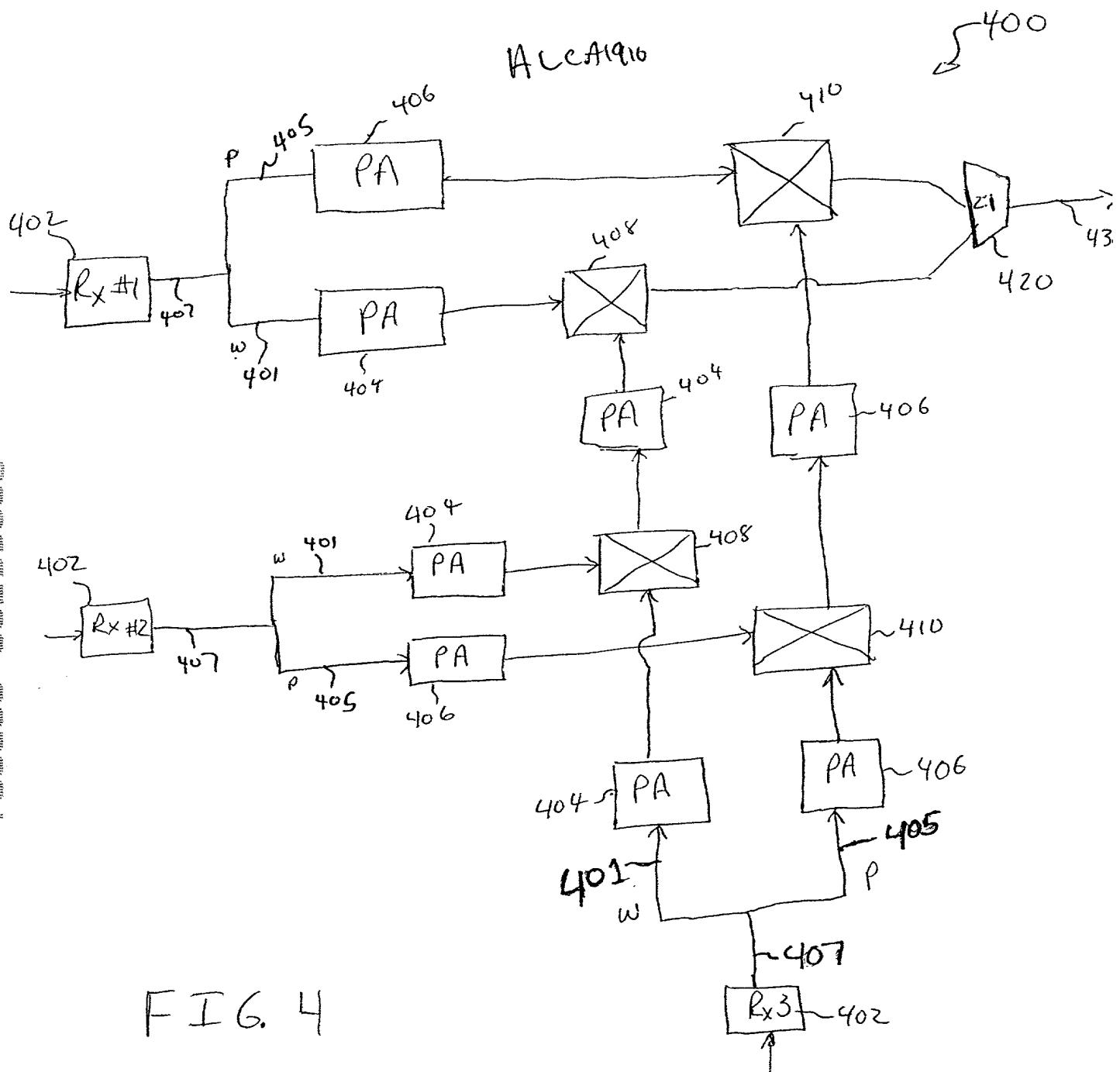


FIG. 4

START

switching matrix

Architecture determined

7.10

System Architect. determines the minimum and maximum STS-offset that will be seen by the working and protect Pointer Adder.

7.20 Software selects an appropriate pointer offset for the working & protect path such that the equations are satisfied (working and protect)

$$\text{PTR_OFFSET} - \text{STS_OFFSET}_{\text{max}} > \text{DEPTH}_{\text{min}}$$

$$\text{PTR_OFFSET} - \text{STS_OFFSET}_{\text{min}} < \text{DEPTH}_{\text{max}}$$

7.30 Software programs the <sup>✓</sup> Pointer Offset into the working and protect Pointer Adder.

7.40 A feedback mechanism in the Pointer Adder ensures that the programmed Pointer Offset is always maintained.

7.50 Frame Alignment is achieved in the system while Pointer Alignment is maintained.

(End)

Fig

77.0

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